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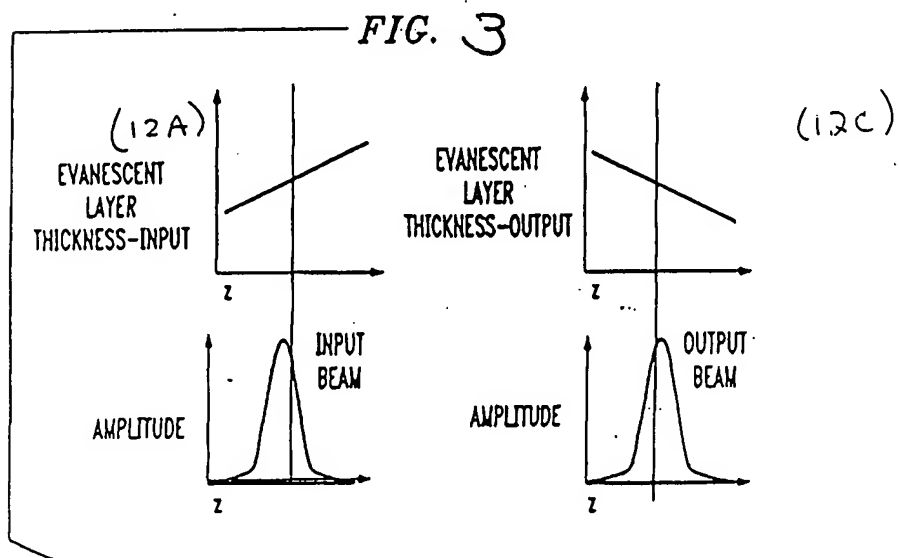
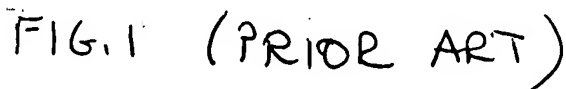
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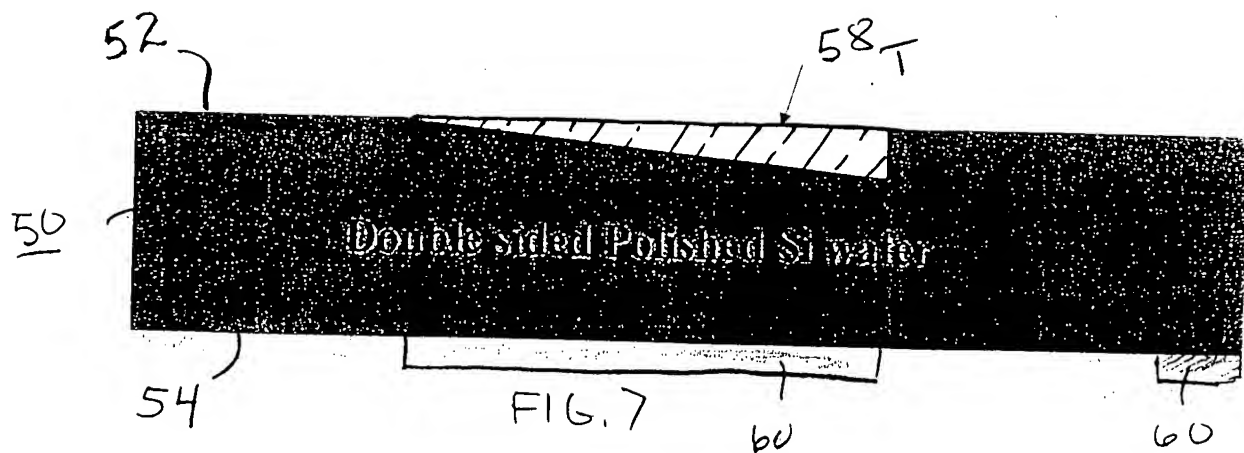
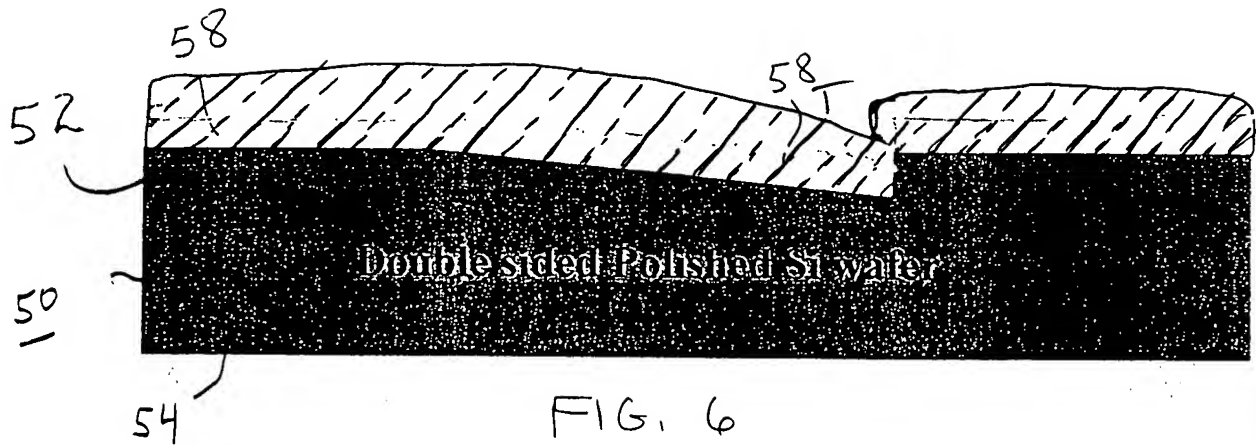
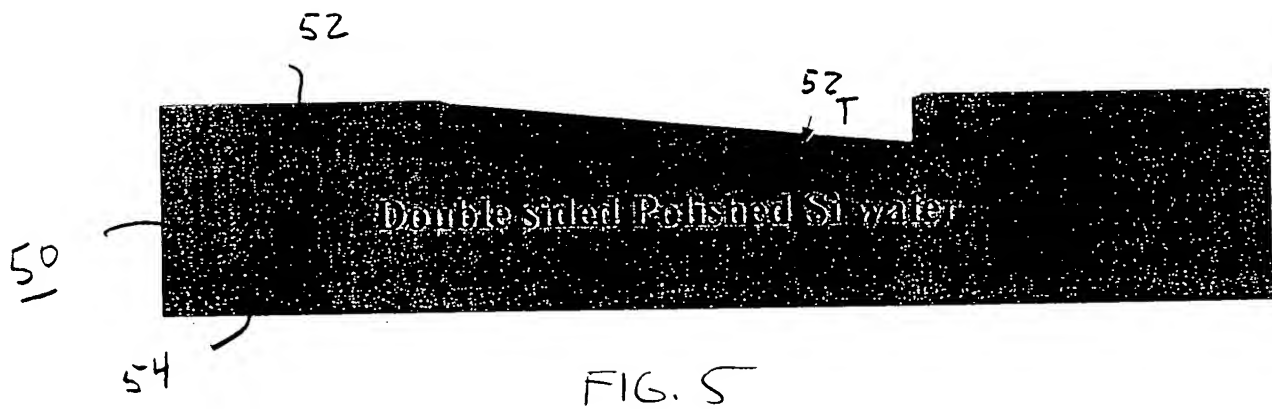
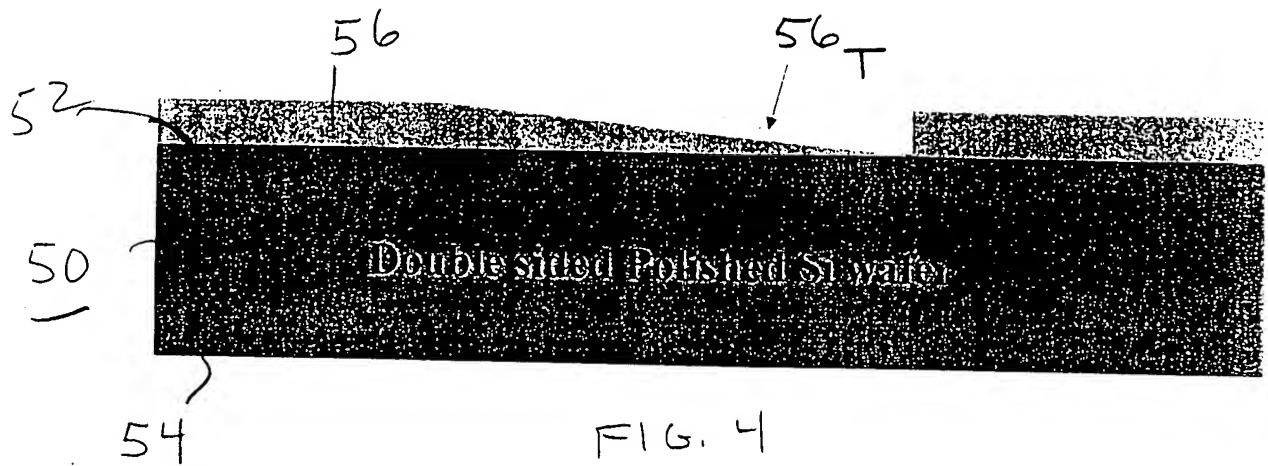
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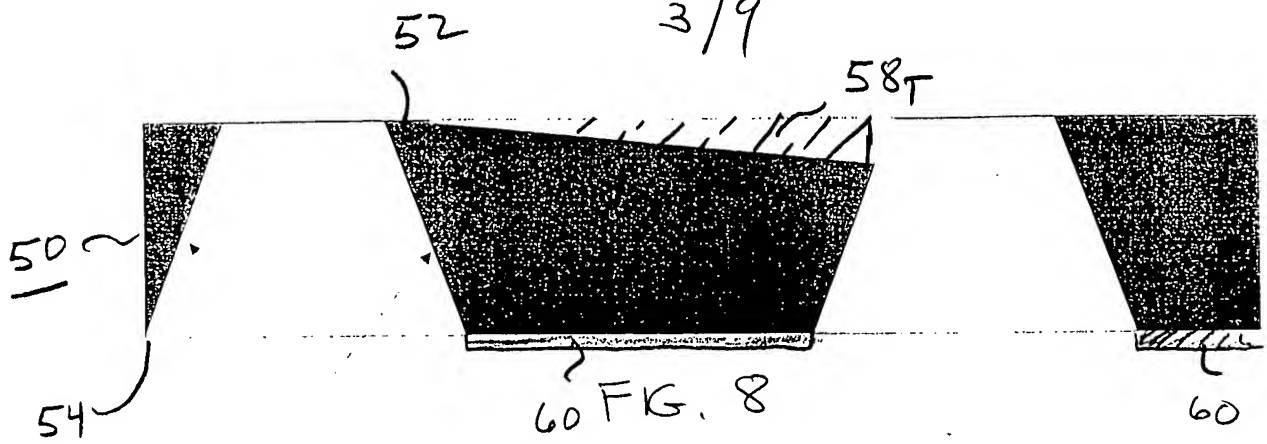
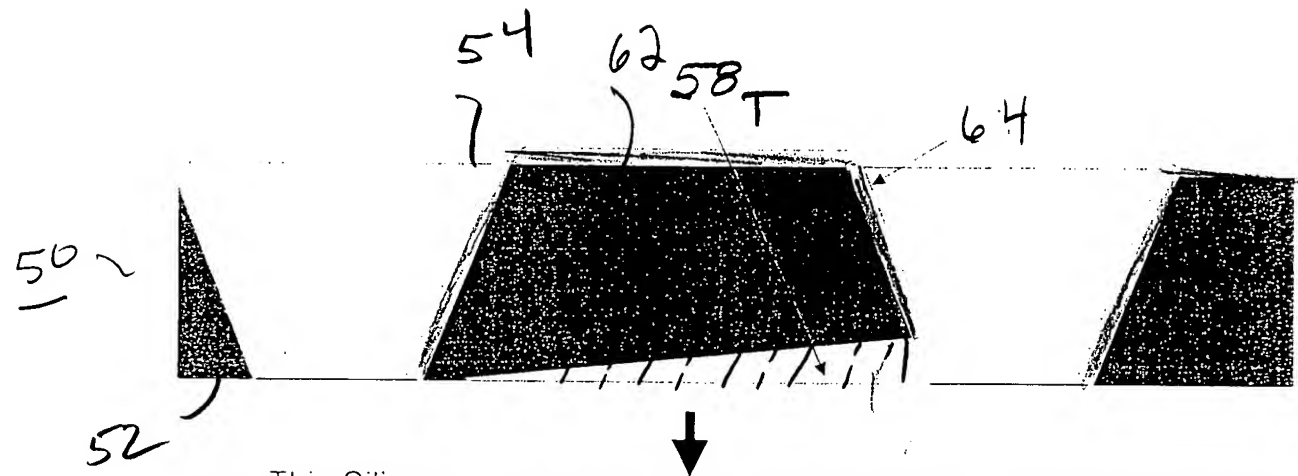


FIG. 8



Thin Silicon

Buried Oxide

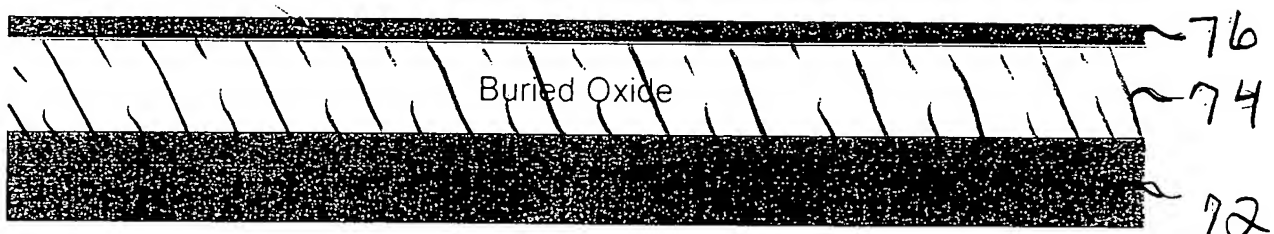


FIG. 9

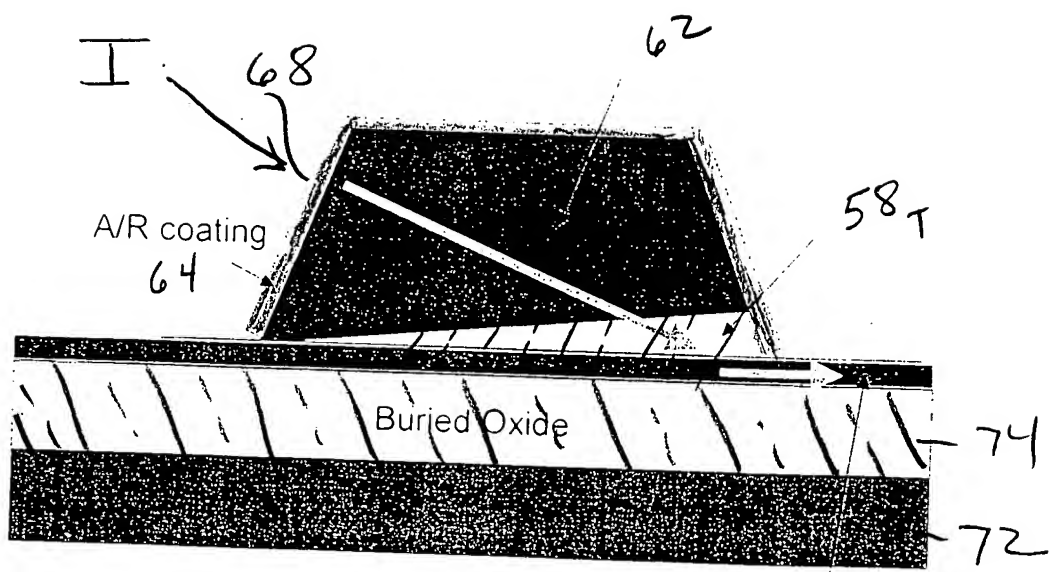
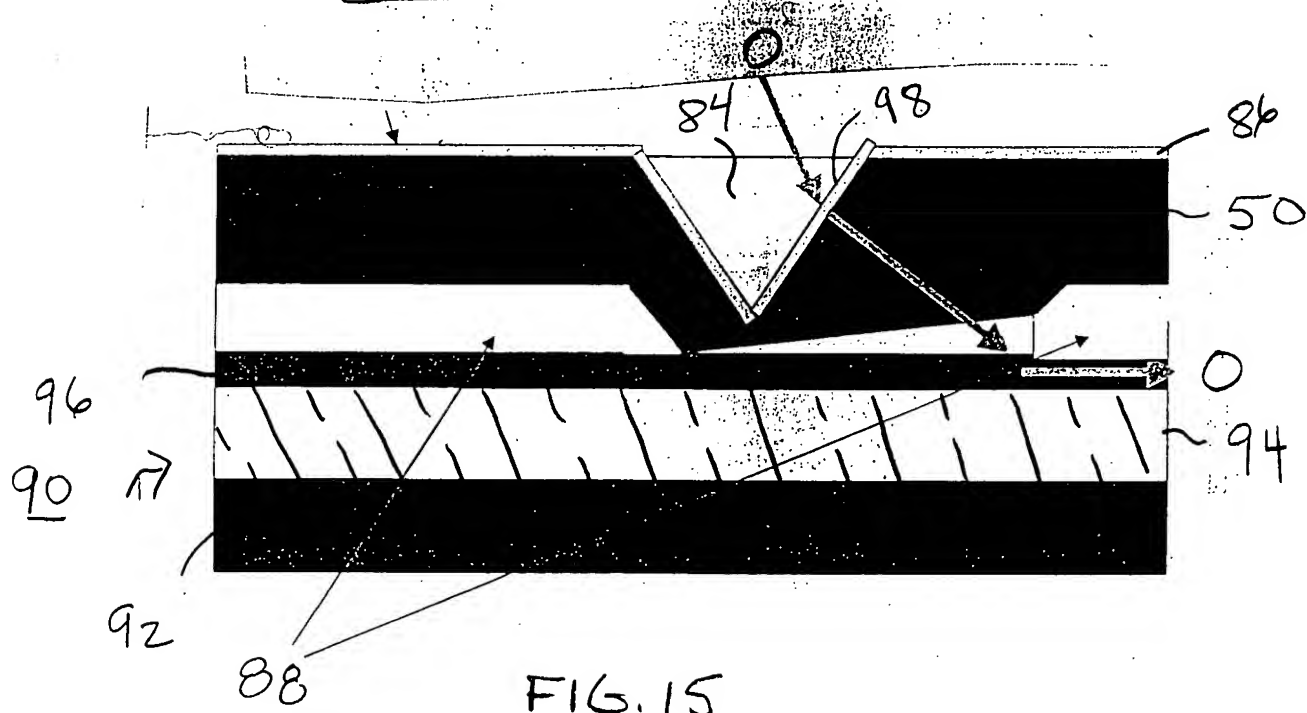
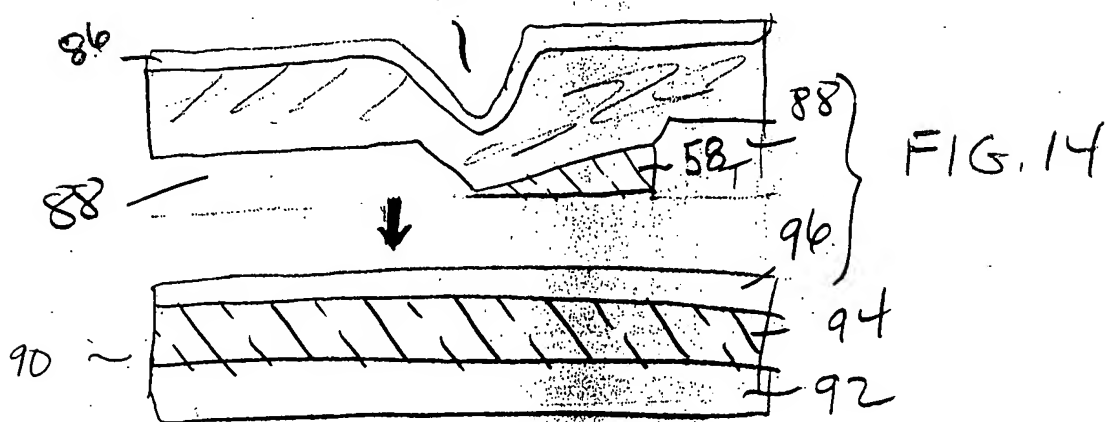
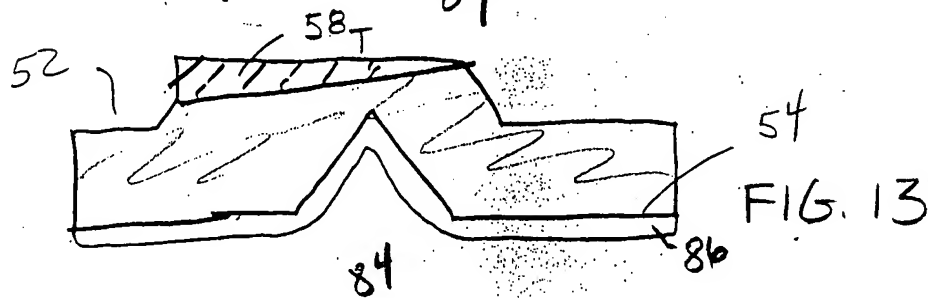
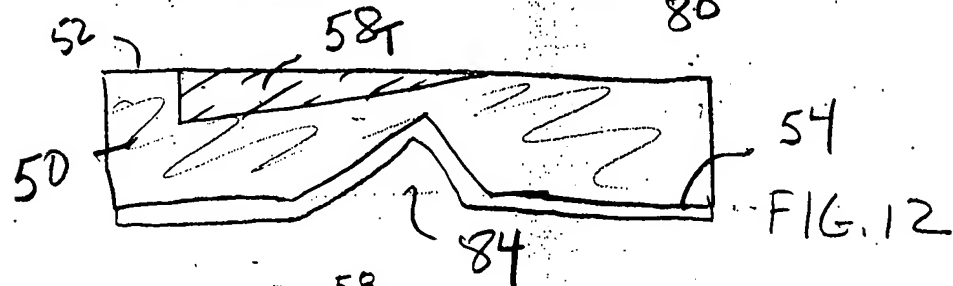
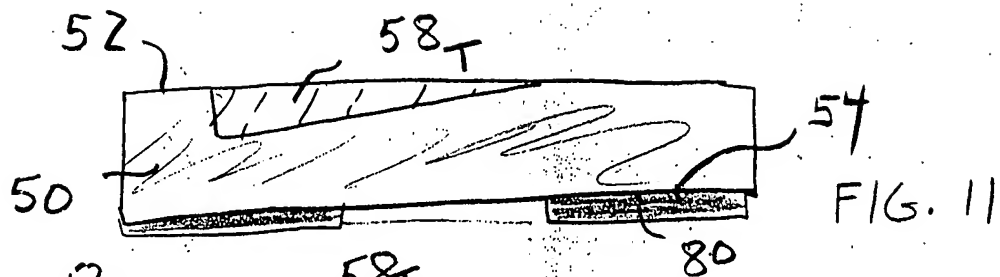


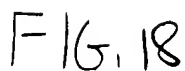
FIG. 10

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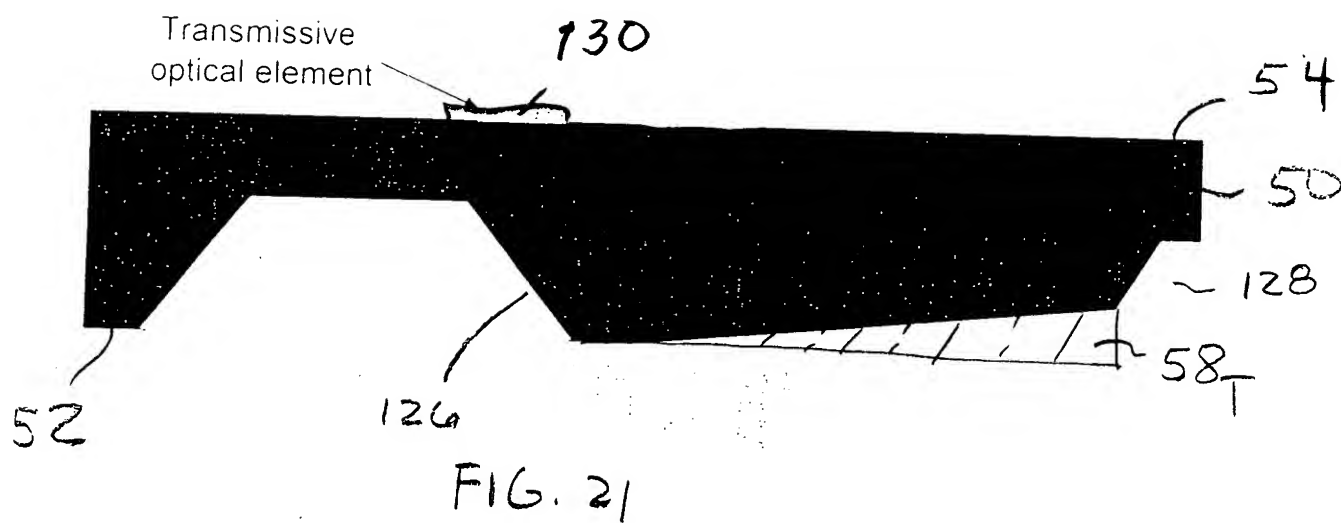
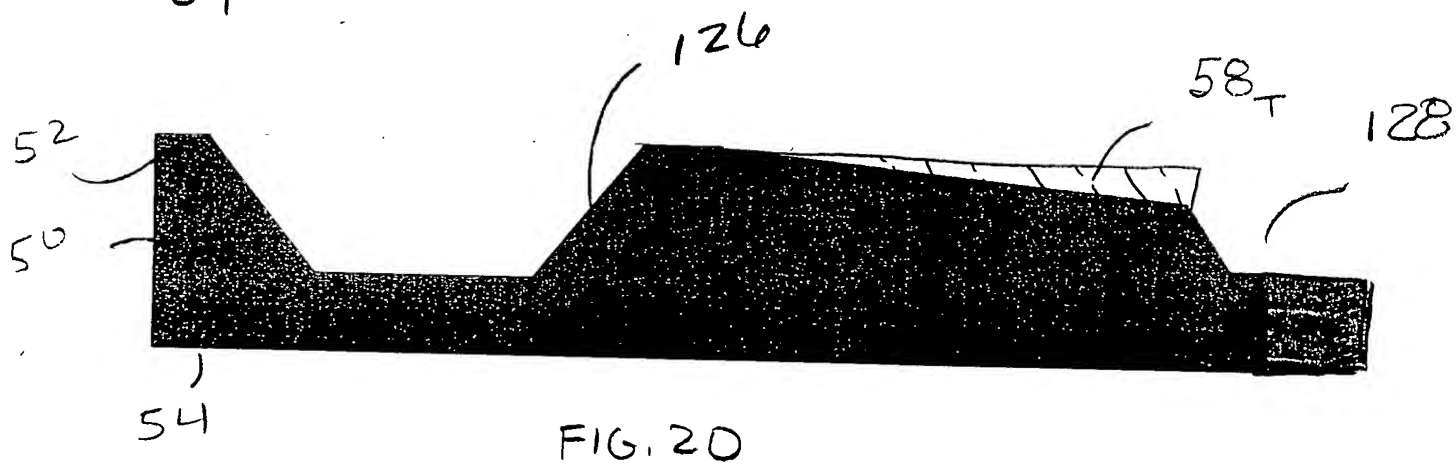
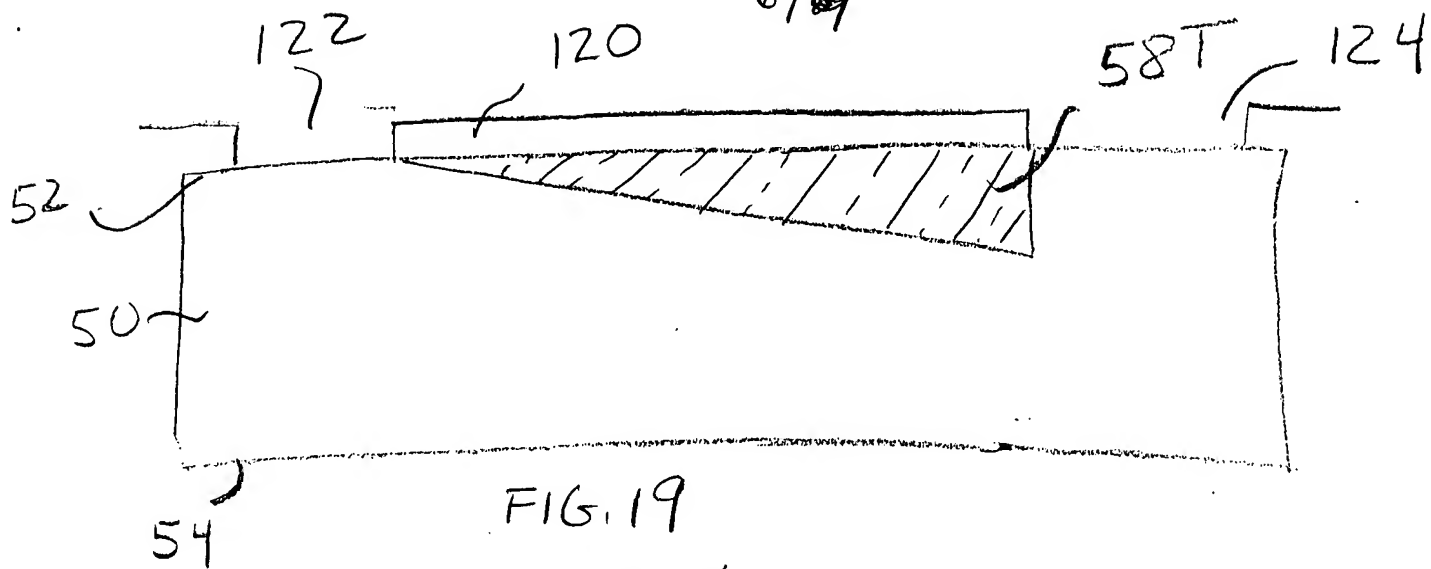
A/R coating

Buried Oxide

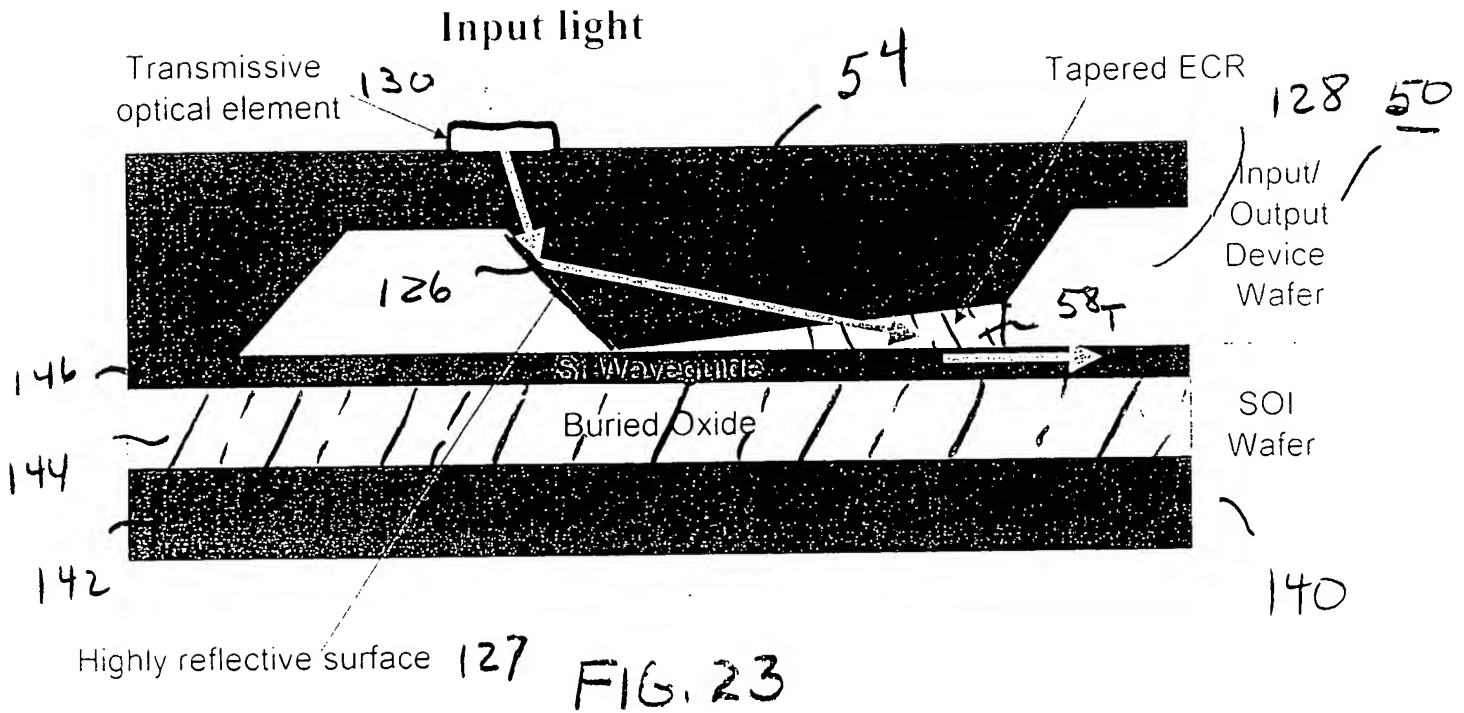
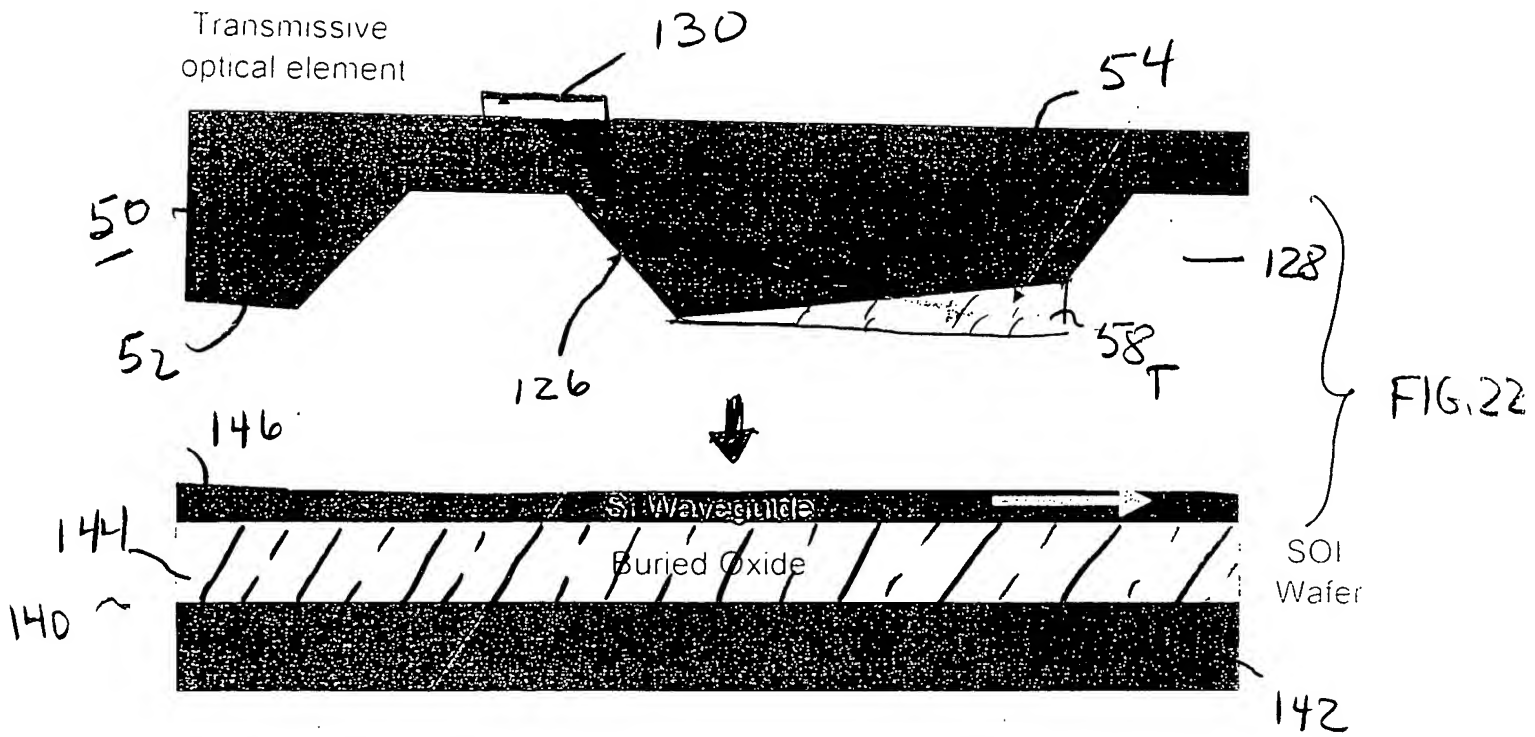




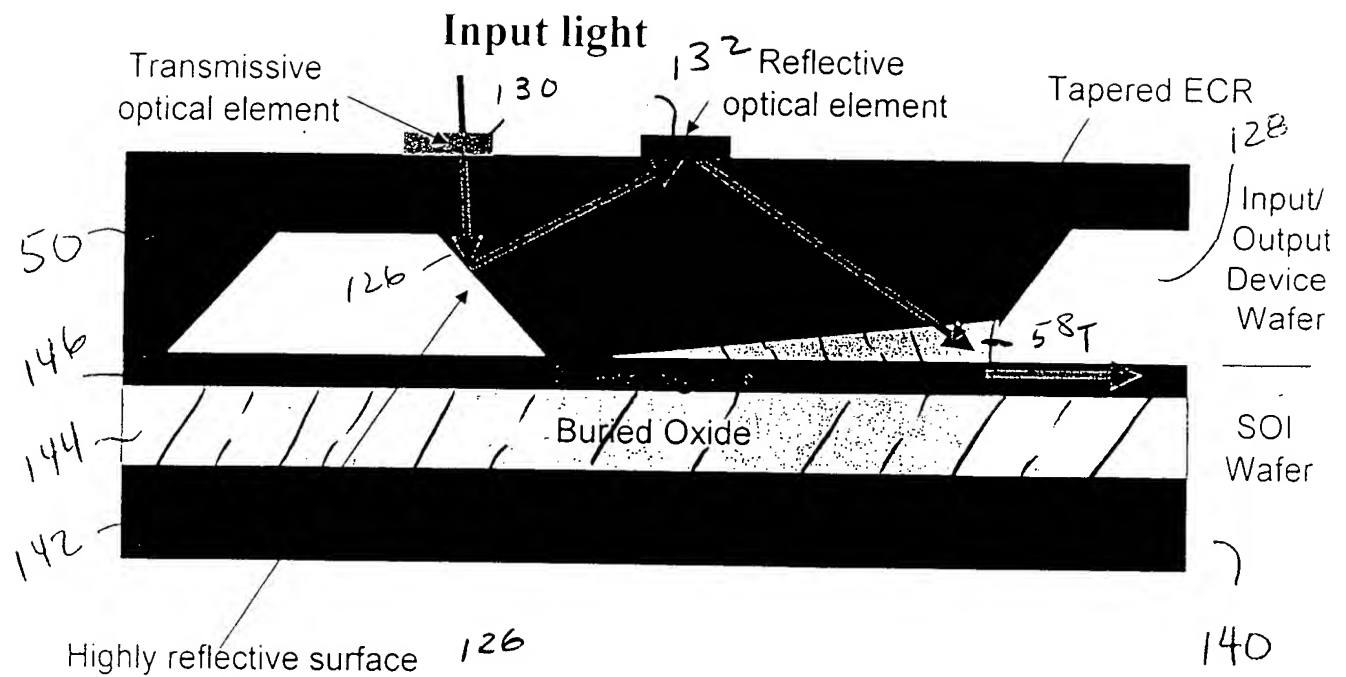
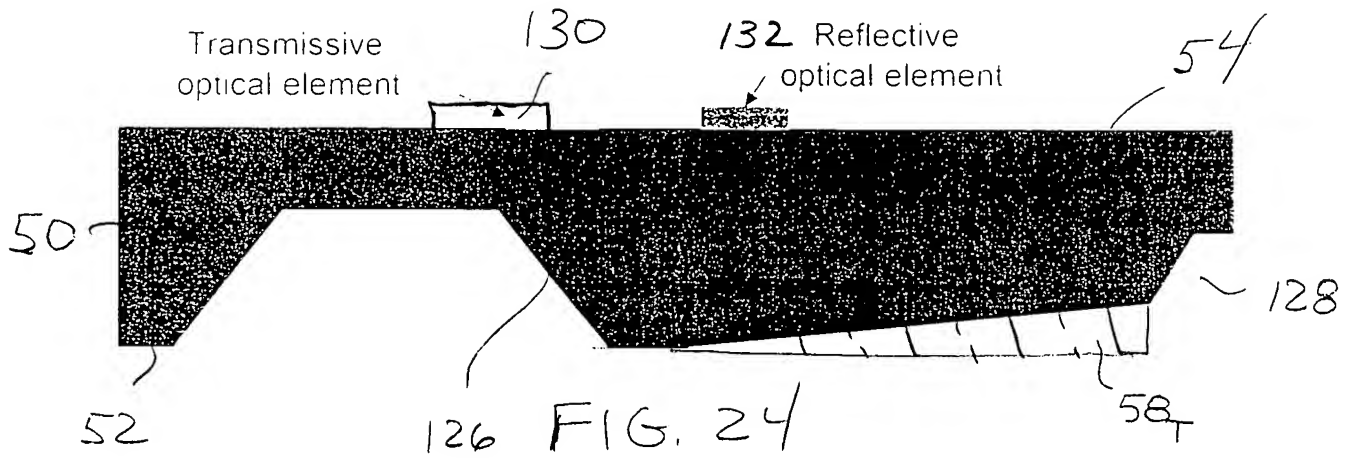
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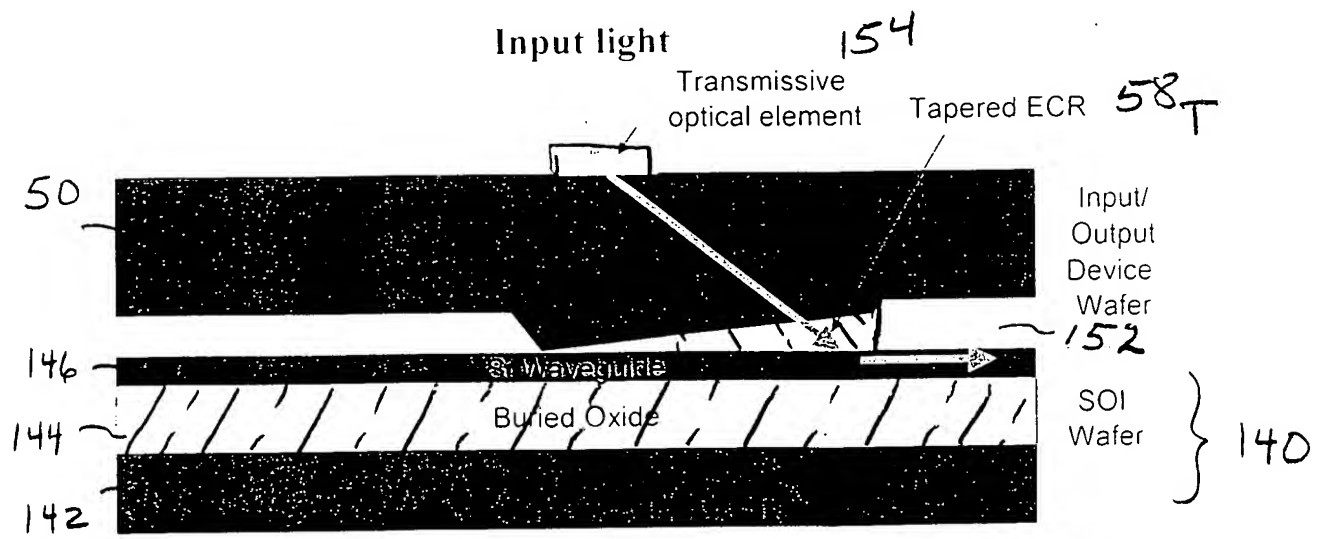
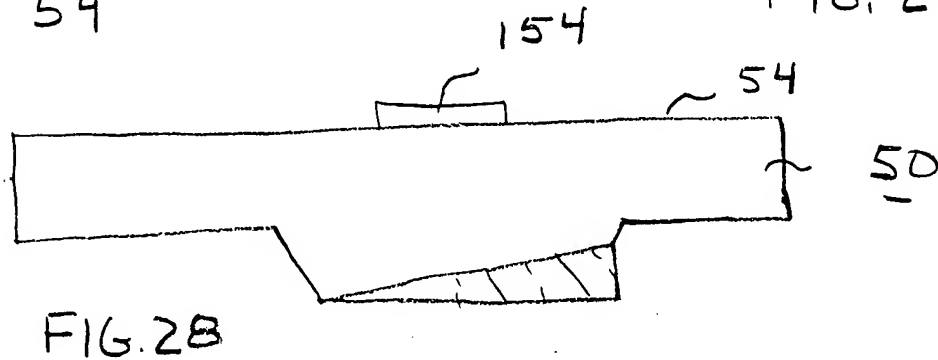
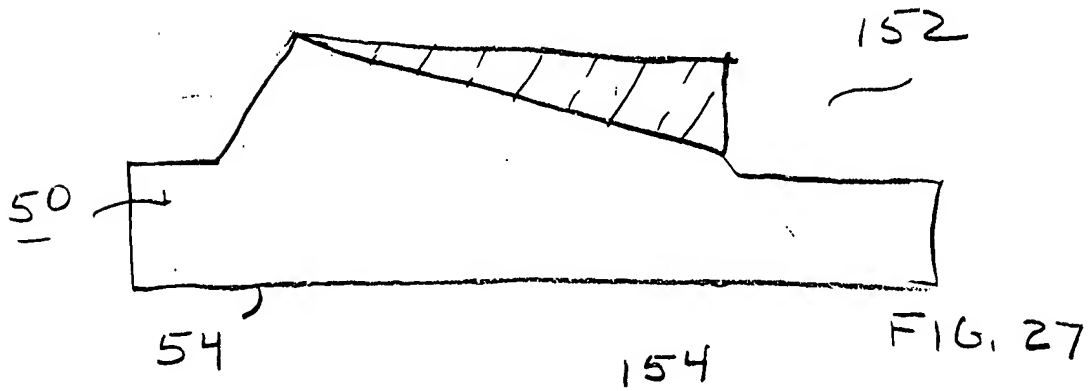
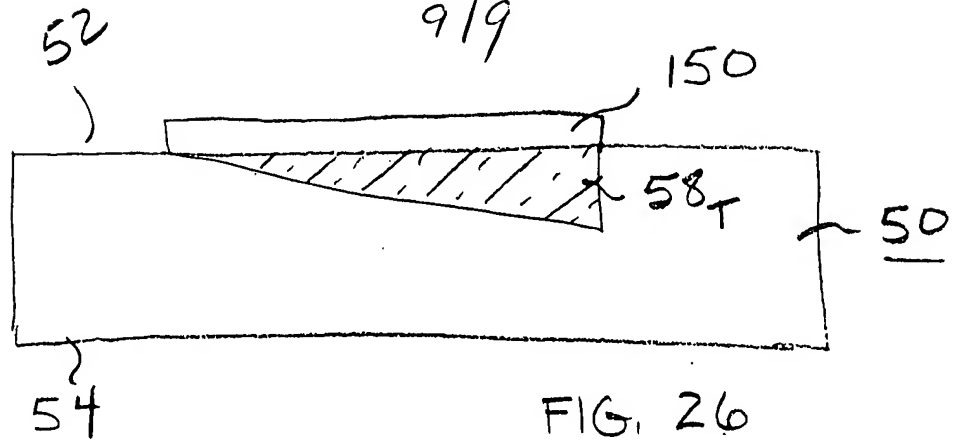


FIG. 29